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UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 003401.P098
 First Inventor or Application Identifier Gaetan L. Mathieu
 Title SHAPED SPRING
 Express Mail Label No. EM014065823US

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents

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1. ☒ Fee Transmittal Form (e.g. PTO/SB/17)
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 (preferred arrangement set forth below) **Total Pages** 56
- Descriptive title of the Invention
 - Cross References to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to Microfiche Appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
3. ☒ Drawing(s) (35 U.S.C. 113) **Total Sheets** 15
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Attorney's Docket No. 003401.P098
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UNITED STATES PATENT APPLICATION

FOR

SHAPED SPRING

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SHAPED SPRING

BACKGROUND OF THE INVENTION

5 Field of the Invention

The invention relates to an interconnection (contact) element suitable for effective connections between electronic components.

10 Description of Related Art

Interconnection or contact elements may be used to connect devices of an electronic component or one electronic component to another electronic component. For example, an interconnection element may be used to connect two circuits of an integrated circuit chip or to connect an application specific integrated circuit (ASIC). Interconnection elements may also be used to connect the integrated circuit chip to a chip package suitable for mounting on a printed circuit board (PCB) of a computer or other electronic device or to connect the integrated circuit chip directly to the PCB.

Interconnection elements may further be used to connect the integrated circuit chip to a test device such as a probe card assembly or other substrate to test the chip.

Generally, interconnection or contact elements between electronic components can be classified into at least the two broad categories of "relatively permanent" and "readily demountable."

An example of a "relatively permanent" contact element is a wire bond. Once two electronic components are connected to one another by a bonding of an interconnection element to each electronic component, a process of unbonding must be used to separate the components. A wire bond interconnection

element, such as between an integrated circuit chip or die and inner leads of a chip or package (or inner ends of lead frame fingers) typically utilizes a "relatively permanent" interconnection element.

5 One example of a "readily demountable" interconnection element is the interconnection element between rigid pins of one electronic component received by resilient socket elements of another electronic component, for example, a spring-loaded LGA socket or a zero-insertion force socket. A
10 second type of a "readily demountable" interconnection element is an interconnection element that itself is resilient or spring-like or mounted in or on a spring or resilient medium. An example of such an interconnection element is a tungsten needle or a micro-spring contact of a
15 probe card component such as described in commonly assigned U.S. Patent No. 5,974,662, titled "Method of Planarizing Tips of Probe Elements of a Probe Card Assembly," issued November 2, 1999 (FFI-P06). The interconnection element of a probe card component is typically intended to effect a
20 temporary pressure connection between an electronic component to which the interconnection element is mounted and terminals of a second electronic component, such as a semiconductor device under test.

 With regard to spring interconnection elements,
25 generally, a minimum contact force is desired to effect reliable pressure contact to an electronic component (e.g., to terminals of an electronic component). For example, a contact (load) force of approximately 15 grams (including as little as 2 grams or less and as much as 150 grams or more,
30 per terminal) may be desired to effect a reliable electrical pressure connection to a terminal of an electronic component.

A second factor of interest with regard to spring interconnection elements is the shape and metallurgy of the portion of the interconnection element making pressure connection to the terminal of the electronic component. With respect to the tungsten needle as a spring interconnection element, for example, the contact end is limited by the metallurgy of the element (i.e., tungsten) and, as the tungsten needle becomes smaller in diameter, it becomes commensurately more difficult to control or establish a desired shape at the contact end.

In certain instances, interconnection elements themselves are not resilient, but rather are supported by a resilient membrane. Membrane probes exemplify this situation, where a plurality of microbumps are disposed on a resilient membrane. Again, the technology required to manufacture such contact elements limits the design choices for the shape and metallurgy of the contact portion of the contact elements.

Commonly-assigned U.S. Patent Application No. 08/152,812, filed November 16, 1993 (now U.S. Patent No. 5,476,211, issued December 19, 1995) (FFI-P01), discloses methods for making spring interconnection elements. In a preferred embodiment, these spring interconnection elements, which are particularly useful for micro-electronic applications, involve mounting an end of a flexible elongate element (e.g., wire "stem" or "skeleton," sometimes "core") to a terminal on an electronic component, and coating the flexible element and adjacent surface of the terminal with a "shell" of one or more materials. One of skill in the art can select a combination of thickness, yield strength, and elastic modulus of the flexible element and shell materials to provide satisfactory force-to-deflection characteristics

of the resulting spring interconnection elements. Exemplary materials for the core element include gold. Exemplary materials for the coating include nickel and its alloys. The resulting spring interconnection element is suitably used to effect pressure, or demountable, interconnections between two or more electronic components, including semiconductor devices.

As electronic components get increasingly smaller and the spacing between terminals on the electronic components get increasingly tighter (the pitch gets increasingly finer), it becomes increasingly more difficult to fabricate interconnections suitable for making electrical connection to terminals of an electronic component. Co-pending and commonly-assigned U.S. Patent Application No. 08/802,054, filed February 18, 1997, titled "Microelectronic Contact Structure, and Method of Making Same" (FFI-P34) (incorporated herein in full by reference), and corresponding PCT application published November 27, 1997 as WO97/44676, disclose a method of making spring interconnection elements through lithographic techniques. In one embodiment, that application discloses forming a spring interconnection element (including a spring interconnection element that is a cantilever beam) on a sacrificial substrate and then transferring and mounting the interconnection element to a terminal on an electronic component. In that disclosure, the spring interconnection element is formed in the substrate itself through etching techniques. In co-pending, commonly-assigned U.S. Patent Application No. 08/852,152, titled "Microelectronic Spring Contact Elements" (FFI-P35), spring interconnection elements are formed on a substrate, including a substrate that is an electronic component, by depositing and patterning a plurality of masking layers to form an

opening corresponding to a shape embodied for the spring interconnection element, depositing conductive material in the opening made by the patterned masking layers, and removing the masking layer to form the free-standing spring interconnection element.

Co-pending and commonly-assigned U.S. Patent Application No. 09/023,859, titled "Microelectronic Contact Structures and Methods of Making Same" (FFI-P047) describes an interconnection element having a base end portion (post component), a body portion (beam component) and a contact end portion (tip component) and methods of separately forming each portion and joining the post portion together as desired on an electronic component.

U.S. Patent No. 5,613,861 (and its counterpart divisional U.S. Patent No. 5,848,685) issued to Smith et al. disclose photolithographically patterned spring interconnection elements formed on a substrate with a body having an inherent stress gradient formed of a resilient (e.g., elastic) material such as chrome-molybdenum alloy or a nickel-zirconium alloy. The stress gradient causes an end of the body to bend away from the substrate in the shape of an arc when the end is freed from the substrate.

In order to achieve the desired shape of the body, Smith et al. must limit the thickness of the interconnection element described in U.S. Patent No. 5,613,861. A limit on the thickness of the interconnection element limits the spring constant, k , of the interconnection element (k increases as thickness increases) particularly in state-of-the-art interconnection element arrays where the dimensions (e.g., length and width) of individual interconnection arrays are reduced to accommodate a corresponding increase in contact pad or terminal density. A reduction of the spring

constant generally reduces the amount of load or force, F , that may be applied to resilient interconnection elements for a given deflection, x ($k=F/x$). Thus, such interconnection elements generally sustain at best a moderate contact force, which may not be enough to effect reliable pressure contact to an electronic component.

What is needed is a resilient interconnection element and a method of improving the resiliency of an interconnection element, particularly interconnection elements that are suitable for present fine-pitch electrical connections and that is/are scalable for future technologies. Also needed are improved methods of making resilient interconnection elements, particularly methods that are repeatable, consistent, and inexpensive.

SUMMARY OF THE INVENTION

An interconnection element is disclosed. In one embodiment, the interconnection element includes a first element material adapted to be coupled to a substrate and a second element material coupled to the first element material. At least one of the first element material and the second element material comprises a material having a transformable property such that upon transformation, a shape of the interconnection element is deformed. One example is a first and/or second element material having a property such that a first volume of the material is adapted to be transformed to a second, different volume.

One portion of the interconnection element of the invention may be coupled to a substrate such as an electronic component and in electrical communication with a signal at the substrate. The interconnection element may adopt a variety of configurations to allow elastic deformation when

interconnecting two electronic components. One preferred embodiment is a configuration of a cantilever where a free end of the interconnection element is available for electrical connection, such as to probe contact pads or terminals of another electronic component or electrically couple two electronic components.

In one aspect of the invention, the first element material and the second element material of the interconnection element are arranged in a configuration such that the second element material overlies the first element material and the first volume is greater than the second volume. In such case, the transformation to a smaller second volume causes the interconnection element to deform along some portion of its length. In the example where the interconnection element adopts a cantilever configuration on a substrate, the volume transformation will cause, in one embodiment, the free end of the interconnection element to deform away (i.e., deflect) from the substrate with the free end arcing toward the coupled base, thus providing a projecting interconnection element. The distance that an interconnection may deflect from the substrate may be limited by, for example, a travel stop so that, in one instance, a consistent deflection distance may be established for a plurality of interconnection elements on a substrate (or above the substrate). Once the shape of the interconnection element is deformed, the second element material may be retained or removed, to yield a bi-material or mono-material interconnection element, respectively.

By providing an interconnection element of a material that undergoes a property transformation to deform the interconnection element, the fabrication of interconnection elements on a substrate may be simplified over prior art

methods. The bi-material or mono-material interconnection element may also serve as a precursor or core to a final interconnection structure, the final structure formed by incorporating additional materials or layers, e.g., resilient layers, to form the interconnection element.

An electronic assembly is also disclosed. In one embodiment, the electronic assembly includes a substrate having a plurality of contact nodes accessible on the substrate and a plurality of free-standing resilient interconnection elements coupled to the substrate in such a manner that an attachment element or a base of an interconnection element electrically contacts a corresponding one of the contact nodes. In another embodiment, the contact nodes are signal lines on or in the substrate. The interconnection element comprises, in one aspect, a first element material adapted to be coupled to a substrate, and a second element material coupled to the first element material. One of the first element material and the second element material comprises a material having a transformable property such that upon transformation, a shape of the interconnection element is deformed. One example is a first and/or second element material having a property such that a first volume of the material is adapted to be transformed to a different second volume.

A method is further disclosed. The method includes creating an interconnection element coupled to a substrate and comprising a first element material and a second element material; releasing the interconnection element from the substrate at one end; and transforming a property of one of the first element material and the second element material to deform the interconnection element. One example of a property transformation is transforming the volume of one of

the first element material and the second element material from a first volume to a second, different volume. In one embodiment, the release of the one end of the interconnection element may occur before or after the transformation step.

- 5 In one aspect, the interconnection element may adopt a variety of configurations such as a cantilever where a free end of the interconnection element is available for electrical connection, such as to probe or electrically couple an electronic assembly. The second element material
- 10 may overly the first element material such that, in one embodiment, a volume transformation, for example, results in the second volume being less than the first volume. In the case of a cantilever interconnection element, the free end of the interconnection element may deform upon transformation to
- 15 move the free end of the contact away from the substrate. Following the transformation, the second element material may be removed to create a mono-material interconnection element. This latter operation is particularly useful where the remaining material is in a state where it will retain, at
- 20 least more or less, the modified shape.

- The interconnection element formed by the method of the invention may stand alone or may serve as a precursor to a final interconnection structure. In one embodiment, the method includes, after the transformation step, patterning a
- 25 masking material over the substrate to have an opening exposing the surface of the interconnection element and coating a third element material, such as a resilient material, on the exposed surface of the interconnection element to increase the spring constant of the
- 30 interconnection element. One suitable masking material is an electrophoretic resist material that may be introduced onto the surface of the substrate. One advantage of using the

electrophoretic resist material as the masking material is that it may be introduced uniformly over the surface without disrupting (e.g., damaging) the interconnection element or elements on the substrate.

5 Products including the new contact elements can be secured to a second electronic component by means of pressure, as in a holding fixture or a probing apparatus for testing wafers. Products also can be secured more permanently, as by soldering, much as in conventional
10 mounting of ICs to system boards.

Additional features, embodiments, and benefits will be evident in view of the figures and detailed description presented herein.

15 BRIEF DESCRIPTION OF THE DRAWINGS

The features, aspects, and advantages of the invention will become more thoroughly apparent from the following detailed description, appended claims, and accompanying drawings in which:

20 **Figure 1** illustrates a cross-sectional side view of a portion of an electronic component having a plurality of contact pads with a shorting layer over the pads.

Figure 2 illustrates the structure of **Figure 1** after the patterning of a masking material over the substrate and
25 having openings to the plurality of contact pads according to a first embodiment of the invention.

Figure 3 shows the structure of **Figure 1** after a thermal treatment.

Figure 4 shows the structure of **Figure 1** after the
30 conformal introduction of an activation layer over the top surface of a structure.

Figure 5 shows the structure of **Figure 1** after the patterning of a second masking material over the top surface of a structure.

Figure 6 shows the structure of **Figure 1** after the introduction of first element material in openings defined by the second masking material.

Figure 7 shows the structure of **Figure 1** after the introduction of second element material over the first element material.

Figure 8 shows the structure of **Figure 1** after the removal of the second masking material.

Figure 9 shows the structure of **Figure 1** after the removal of exposed adhesion layer material.

Figure 10 shows the structure of **Figure 1** after the removal of the first masking material.

Figure 11 shows the structure of **Figure 1** after inducing a volumetric change in the second element material.

Figure 12 shows the structure of **Figure 1** after the conformal deposition of a third masking material.

Figure 13 shows the structure of **Figure 1** defining the third masking material.

Figure 14 shows the structure of **Figure 1** after the definition of the third masking material to have an opening to the second element material.

Figure 15 shows the structure of **Figure 1** after the introduction of third element material over the exposed portions of the second element material.

Figure 16 shows the structure of **Figure 1** after the introduction of a probe material over the third element material.

Figure 17 shows the structure of **Figure 1** after the removal of the third masking material.

Figure 18 shows the structure of **Figure 1** after the introduction of travel stop material.

Figure 19 shows the structure of **Figure 1** after patterning the travel stop material.

5 **Figure 20** shows the structure of **Figure 1** after introduction of a second contact material.

10 **Figure 21** shows a graphical representation of the deflection distance of an interconnection element of a first element material of palladium/cobalt alloy of 200 micro-inches (about 5 μm) and varying thickness of a second element material of nickel/cobalt alloy.

15 **Figure 22** illustrates a cross-sectional side view of a portion of an electronic component having a shorting layer over contact pads, a masking material over the shorting layer, an activation layer over the masking material, and first element material introduced and patterned over the activation layer in accordance with another embodiment of the invention.

20 **Figure 23** shows the structure of **Figure 22** after the introduction of a shape memory alloy (SMA) material in a martinsite state over the patterned first element material.

Figure 24 shows the structure of **Figure 22** after the patterning of the SMA material.

25 **Figure 25** shows the structure of **Figure 22** after the removal of the first element material.

Figure 26 shows the structure of **Figure 22** after the transformation of the SMA material to its memory state.

Figure 27 shows the structure of **Figure 22** after the removal of the SMA material.

30 **Figure 28** shows the structure of **Figure 22** after the introduction of probe material and contact material to each

interconnection element and the patterning of travel stop material.

DETAILED DESCRIPTION OF THE INVENTION

5 The invention relates to interconnection elements, including contact elements. According to one aspect of the invention, at least initially, an interconnection element includes at least two materials and one or both of the materials is adapted to undergo a transformation to deform
10 the shape of the interconnection element. One example is a volume transformation of at least one of the materials from a first volume to a second, different volume, the volume transformation bringing about a change in the shape of the interconnection element. One advantage of the
15 interconnection element of the invention is that such a structure may be formed with the desired shape, resilience, and spring constant according to a simplified technique.

In this manner, the invention describes an interconnection element having improved characteristics over
20 prior art interconnection elements thus improving the suitability of the interconnection element of the invention for use in present and future, reduced-sized applications, including providing a conductive path between electronic components such as in contacting and/or testing of contact
25 pads or terminals of an electronic component.

The following discussion describes the transformation (e.g., volume transformation) of at least one of two interconnection materials to deform (e.g., modify) the shape of the interconnection element. In terms of at least a two
30 material structure wherein a second material overlies a first material, several transformation scenarios are possible. Such scenarios include, but are not limited to, a second

element material that undergoes a volume transformation to a smaller volume; a first element material that undergoes a volume transformation to a larger volume; a first element material and a second element material (and possibly additional materials) that each undergo a volume transformation to deform an interconnection element; and two materials with different stress properties that will deform the shape of an interconnection element, for example, through a heat treatment. In general, the behavior of metallic materials under temperature changes is understood in the art and the invention capitalizes on this behavior in designing interconnection elements with improved resiliency and spring constants in a simplified manner.

In the embodiments that follow, suitable electronic components include, but are not limited to, an active semiconductor device; a memory chip; a portion of a semiconductor wafer including a semiconductor wafer or die, made of any suitable semiconductor material such as silicon (Si) or gallium arsenide (GaAs); a ceramic substrate; an organic substrate; a printed circuit board (PCB); an organic membrane; a polyimide sheet; a space transformer; a probe card; a chip carrier; a production interconnect socket; a test socket; a sacrificial member; a semiconductor package including, but not limited to, ceramic and plastic packages and chip carriers; and a connector. The electronic component may be an active device or a passive device that supports one or more electronic connections. In general, suitable electronic components include, but are not limited to, devices comprising an integrated circuit having at least two contact pads or terminals providing electrical access to the circuit. Such a device is representatively demonstrated by an integrated circuit chip (or micro-chip) having a plurality

of exposed contact pads or terminals providing access to the integrated circuit of the device.

The interconnection element of the invention may be fabricated on or independent of the electronic component to which it is joined. In the case of independent fabrication, the invention contemplates that the interconnection element or elements can be fabricated with a shape, size, and metallurgy that are not limited by the materials and layout considerations associated with the manufacture of the electronic component. Independent fabrication also avoids the exposure of the electronic component to the process conditions associated with forming the interconnection element. In the case of fabricating directly on the substrate, the invention contemplates that the interconnection elements can be formed directly on contacts or terminals on the substrate, thus reducing transferring operations and reducing alignment issues.

Disposed on an electronic component such as a space transformer of a probe card assembly, the interconnection elements of the invention are designed to accommodate contact pads or terminals of electronic components having very small pitch or spacing tolerances. In one embodiment, the interconnection elements of the invention adopt alternating orientation (e.g., left-right-left-right) so as to achieve a greater pitch between their post portions than between their tip portions. In another embodiment, the interconnection elements of the invention adopt alternating lengths (e.g., short-long-short-long) so as to achieve a greater pitch between a post or anchor portion than at the tip portion of adjacent interconnection elements. Similarly, alternating interconnection elements can be fabricated to have a greater pitch at their tip portions than their post or anchor

portions. In summary, the interconnection elements, whether fabricated on or independent of the electronic component to which they are joined may adopt a variety of orientations to accommodate various configurations associated with the

5 electronic components to which they contact.

Figure 1 shows a cross-sectional side view of electronic component or structure 100. Structure 100 is, for example, a space transformer of a probe card assembly or an integrated circuit. Structure 100 includes, in this example,

10 semiconductor-, organic-, metal-, or ceramic-based substrate 110 having contact pads or terminals 120 on a surface of substrate 110. In the case of a commercially available ceramic-based electronic component (e.g., a multi-layer ceramic substrate such as a low-temperature, co-fired ceramic

15 (LTCC) substrate), structure 100 may contain corresponding contact pads or terminals 121 on opposing surfaces of substrate 110. Corresponding contact pads or terminals 121 on opposing surfaces are connected, for example, through

20 conductive circuit 119 running through substrate 110 such as, for example, a circuit of molybdenum, copper, gold, nickel, or tungsten and aluminum or combination of these materials or their alloys or other conductive element and its alloys. Details of materials, thickness, processing variations and the like can be found in co-pending, commonly-owned U.S.

25 Patent Application No. 09/032,473, filed February 26, 1998, titled "Lithographically Defined Microelectronic Contact Structures" (FFI-P048), and PCT equivalent published as WO98/52224 on November 9, 1998, both of which are incorporated herein by reference.

30 Contact pads or terminals 120 on substrate 110 are, for example, copper, nickel, and gold that may be suitable for connecting to an interconnection element formed to the

deposition of a conductive material by, for example, sputtering, electroplating or soldering. In one example, the copper facilitates the electroplating process and is the upper layer.

5 In this embodiment, overlying substrate 110 of structure 100 is shorting layer 130. Shorting layer 130 is, for example, a copper (Cu), titanium (Ti) or titanium-tungsten (Ti-W) or other appropriate metal or alloy that serves, in one sense, to short contact pads or terminals 120 during the
10 formation of interconnection elements on substrate 110. As will become evident from the description that follows, the shorting feature of shorting layer 130 can advantageously be employed to establish an appropriate potential for an electrolytic process (e.g., an electroplating process) for
15 fabricating interconnection elements on substrate 110. It is to be appreciated that an electrolytic process is one technique for forming the interconnection elements of the invention, and that other techniques may also be suitable, such as chemical or sputter deposition or electroless plating
20 processes. It is to be appreciated that the operations indicated can be carried out either simultaneously or sequentially on both surfaces of substrate 110, to create a "double-sided" spring device. In this case, re-distributions and a non one-to-one relationship may exist between contact
25 structures and/or interconnection elements on opposing surfaces.

In one embodiment, shorting layer 130 is introduced to a thickness of about 3000Å to 6000Å, such as about 5000Å over a surface of substrate 110 by, for example, sputtering. One
30 alternative may be to introduce shorting layer 130 over the opposite side of substrate 110 to short contact pads or terminals 121 that extend through substrate 110.

Figure 2 shows structure 100 after the deposition of first masking material layer 140 over the surface of substrate 110. In one embodiment, first masking material layer 140 is a photoresist that is spin-coated on the surface of substrate 110 and patterned as known in the art. First masking material layer 140 is deposited and patterned to include openings. The openings may be located at a position that is directly over a portion of contact pads or terminals 120 or, in some cases, may be located at a position that is remote from contact pads or terminals 120. By locating openings at a position remote from the contact pads or terminals, interconnection elements can be fabricated on an electronic component with a layout that differs from that of the contact pads or terminals of the electronic component. An example of specifics for re-routing the contact pads or terminals are described in co-pending U.S. Patent Application No. 08/955,001, filed October 20, 1997, titled "Electronic Component Having Resilient Contact Elements at Areas Remote from Corresponding Terminals" (FFI-P041) assigned to the assignee of this invention and incorporated herein by reference. This reference also includes a discussion of useful masking materials.

In one embodiment, first masking material layer 140 is deposited to a thickness of approximately 15-25 microns (μm). First masking material layer 140 is generally deposited to a thickness of the desired minimum spacing distance between the body of a subsequently-formed interconnection element and the surface of substrate 110. In the example where resiliency is desired, the thickness of first masking material layer 140 should be chosen such that potential "bottoming out" of the interconnection element is minimized.

In the embodiment where first masking material layer 140 is a photoresist, after patterning and developing the photoresist to have openings to contact pads or terminals 120 (or re-routed suitable openings), the photoresist layer may be subjected to a re-flow or other process to slope the edges of the openings. Creating sloped edges tends to reduce the number of stress points associated with an interconnection element formed through the openings. A re-flow process also introduces additional cross-linking in the photoresist which strengthens the photoresist to improve its properties for subsequent processing operations. In one example, the re-flow is accomplished in stages. At a first stage, structure 100 is brought to a temperature of approximately 105°C then ramped to 110°C and then ramped and retained for one hour at 120°C. In a second stage, the temperature is increased to 130°C for another hour. It is to be appreciated that the temperature of the re-flow process can be adjusted higher or lower to adapt to subsequent process operations.

One alternative to a re-flow process is to slope the edges of the openings in first masking material layer 140 with a sputter or chemical etch.

Figure 4 shows structure 100 after the introduction of activation layer 150. In one aspect, activation layer 150 is introduced to activate the exposed surfaces for introduction of subsequent material by way of an electroplating process. In one embodiment, activation layer 150 is formed by sputtering a material such as chromium/titanium-tungsten/gold (Cr/TiW/Au) layers. Titanium/gold (Ti/Au) or chromium/gold (Cr/Au) may also be utilized. An alternative to activating the surface structure 100 through sputter deposition is to chemically activate the surface. In one example, a palladium/tin (Pd/Sn) chloride bath introduces a film on the

surface of structure 100 and electroless plating of palladium (Pd) is introduced to form a conductive film.

Figure 5 shows structure 100 after the introduction and patterning of second masking material layer 160. In one aspect, second masking material layer 160 defines the shape of the body of a subsequently-formed interconnection element.

Figure 6 shows structure 100 after the introduction of first element material 170. In one example, first element material 170 is a thermally-stable layer. One particular preferred material is a resilient alloy material of, for example, palladium/cobalt (Pd/Co) that generally will not change dimension upon exposure to temperatures to which it may be exposed, is generally inert, and is relatively easy to activate. Other possible materials for first element material 170 include nickel, tungsten, rhodium, iron, copper, silver, and aluminum, preferably in alloy form. In another embodiment, activation layer 150 may be used as the thermally stable layer.

In the embodiment described, wherein first element material 170 is Pd/Co material, first element material 170 is introduced through an electroplating process as known in the art. An introduction of first element material to a thickness of about 50 to 100 micro-inches (about 1-3 μm) is suitable. Alternatively, first element material 170 is made up of a plurality of layers, such as a first layer of a palladium/cobalt alloy and a second layer of palladium.

Figure 7 shows structure 100 after the introduction of second element material 180 over the superior surface of first element material 170. In one aspect, second element material 180 includes a property such that its volume can be transformed from a first volume to a different second volume. The volume transformation may be reversible or irreversible.

Irreversibility may be achieved, for example, through a re-crystallization of the material. One suitable material is a nickel/cobalt (Ni/Co) alloy in a 65/35 atomic percentage ratio with properties which optionally are tailored by plating with additives such as, for example, a brightener such as saccharin, naphthalene-tri-sulfonic acid (NTSA), or other Class 1 and Class 2 brighteners, as well as levelers such as 2-butyne-1,4-diol and thiourea, and salts such as nickel bromide and nickel chloride. The use of additives in a suitable second element material is described in detail in copending U.S. Patent Application No. 09/217,589, filed December 22, 1998, titled "Method of Making a Product with Improved Material Properties by Moderate Heat Treatment of a Metal Incorporating a Dilute Additive" (FFI-P036D1), assigned to the assignee of this invention and incorporated herein by reference in its entirety.

The introduced amount of first element material 170 and second element material 180 is selected, in one embodiment, to control the amount of deformation (e.g., deflection) of the interconnection element structure away from the substrate. As noted above, second element material 180 is transformed from a first volume to a second, different volume. In the case of Ni/Co, the volume change is irreversible and the second volume is smaller or different (in an x, y, z coordinate sense) than the first volume.

It has been determined that the ratio of thickness of first element material 170 to second element material 180, at least with first element material 170 of Pd/Co and second element material 180 of Ni/Co, is important for a chosen deflection. **Figure 21** shows the amount of deflection ($\delta(h_{180})$) in thousandths of an inch of an interconnection element of a cantilever beam having first element material

170 of Pd/Co and a thickness of 200 micro-inches (about 5 microns) and varying thicknesses of second element material 180 of Ni/Co according to the relation:

$$\delta(h_{180}) = \frac{\epsilon \cdot \ell^2}{\left[\frac{(h_{170} + h_{180})^3}{6h_{170} \cdot h_{180}} + \frac{h_{170} + h_{180}}{2} \right]}$$

- 5 wherein ϵ is the percentage of shrinkage, ℓ is the length of the interconnection element beam, h_{170} is the thickness of first element material 170, and h_{180} is the thickness of second element material 180. In this example, the interconnection element is a rectangular beam of length of approximately 40 mils (about 1000 μm). For first element material 170 of 200 micro-inches (about 5 μm), the thickness of second element material 180 may range from approximately 125 to 250 micro-inches (about 3-6 μm) of Ni/Co to yield a deflection distance ($\delta_2(h_{180})$) on deformation of approximately 0.008 inches or 8 mils (about 200 μm). Thus, in this example, the ratio of first element material 170 to second element material 180 may vary between about 1.6 to 1 to about 0.8 to 1. Less of additional second element material 180 will produce less deflection in this example. Varying the amount of first element material 170 may dramatically affect the deflection distance.

- Selecting a first element material 170 thickness that allows for a range of thickness of second element material 180 alleviates some concern of the exactitude of a given deposition, since so long as the final structure is within the calculated ratio with a reasonable tolerance, a desired deflection should be realized. Thus, concerns over plating thickness uniformity or optimizing a thickness of an electroplated structure (such as by chemical-mechanical or mechanical polish) may be reduced or avoided. In the latter

case, if such optimization is desired, a polish may be suitable provided such polish is compatible with the scale of the optimization, e.g., polishing a few micro-inches (less than 1 μm).

5 The above discussion described the transformation of second element material as a thermal transformation. The transformation is believed to be a result of a reorientation of the crystalline structure that makes up the second element material. It is to be appreciated that the volume of a
10 suitable second element material may also be changed by other external events, such as increased pressure or the introduction of radiation such as ion-beam, electron-beam, infrared or ultra-violet radiation.

15 In the above discussion, first element material 170 comprises a Pd/Co alloy and second element material 180, a Ni/Co alloy overlying first element material 170. Alternatively, assuming the same orientation, first element material 170 may constitute Ni/Co alloy and second element material 180 Pd/Co alloy.

20 In one embodiment, first and second element materials 170 and 180 are introduced such that stress varies through the composite beam element 185, with the stress becoming increasingly tensile in the direction away from the substrate. In one case, first element material 170 comprises
25 a Ni/Co alloy that when introduced has a stress that ranges from tensile to compressive. In an introduction process including plating, additives (such as saccharin) may be added to the plating bath to increase the compressive stress of Ni/Co as desired. Second element material 180 comprises a
30 Pd/Co alloy that when introduced has a stress that is more tensile than that of first element 170. Sufficient tensile stress is inherent in a plated Pd/Co alloy but additional

stress may be introduced through additives as known in the art. In a preferred embodiment, the stress in first element material 170 is compressive and the stress in second element material 180 is tensile. Amounts of first element material 170 and second element material 180 on the order of approximately 0.04 to 20 mils (about 1 to 500 μm) are suitable. Suitable thickness ratios may be experimentally determined.

Following the introduction and patterning of first element material 170 and second element material 180, structure 100 is heated, in one example, to 40°-100°C for a period of time sufficient to relax first element material 170 of a Ni/Co. The moderate heating of Ni/Co causes the material to, it is believed, creep to a more relaxed form. Creep in a material is defined generally as the time-dependent part of strain resulting from stress.

At the elevated temperature, first element material 170 creeps under the tensile stress exerted by second element material 180. This causes interconnection element 185 to deform (e.g., deflect away from the surface of substrate 110) and reduces the magnitude of the stress in the first and second element materials 170 and 180. Thus stress relaxation can be considered the driving force for the deformation.

Figure 8 shows structure 100 after the removal of second masking material layer 160. In the example where second masking material layer 160 is a photoresist, the removal of second masking material layer 160 may be accomplished with an oxygen plasma etch (e.g., oxygen ashing). Other methods of removing second masking material layer 160 include, but are not limited to, laser ablation and wet chemical etching. Once second masking material layer 160 is removed, activation layer 150 is removed in those areas previously protected by

second masking material layer 160. Activation layer 150 may be removed, for example, by a chemical or sputter etch as known in the art. **Figure 9** shows structure 100 after the removal of activation layer 150 in exposed areas.

5 **Figure 10** shows structure 100 after the removal of first masking material layer 140. In the example where first masking material layer 140 is a photoresist, first masking material layer 140 may be suitably removed by various methods including an oxygen plasma etch, laser ablation, and wet
10 chemical etching. Once removed, **Figure 10** shows substrate 110 having a plurality of interconnection elements 185 formed on a surface thereof.

Figure 11 shows structure 100 after subjecting the structure generally to a thermal treatment to induce a
15 transformation of one or both of first element material 170 and second element material 180. In the embodiment illustrated, the transformation is a volume transformation of second element material 180. The volume transformation proceeds, in one embodiment, to at least 90 percent
20 completion and preferably 90-95 percent completion. The volume transformation of second element material 180 may be estimated by determining the linear coefficient of thermal expansion of the material. In one example, a second element material of nickel/cobalt deposited to a thickness of about
25 125 to 175 micro-inches (about 3-4 μm) will experience a linear change of about 0.2 percent upon heat treatment at 310°C for ten minutes. Such linear change causes the volume to shrink while the volume of first element material 170, in one embodiment, remains relatively constant. The volume
30 change causes the free end of the interconnection element to bend toward its secured base. A suitable curvature measured

by a distance, h_1 , from the surface of substrate 110 is, for example, approximately 6 to 8 mils (150-200 μm).

As illustrated in **Figure 11**, the volume transformation of second element material 180 will modify the shape of interconnection element 185. In other words, the volume transformation of second element material 180 with the free end of interconnection element 185 tending to curve towards its base will similarly deform first element material 170 and activation layer 150 of interconnection element 185.

In some instances it may be desirable to limit the amount of curvature of second element material 180. Nevertheless, in one embodiment, it is desirable to achieve greater than or equal to 90 percent transformation or approximately 90-95 percent transformation to, for example, form a reliable interconnection element, e.g., reliable in the sense of deformation characteristics. In such cases, a curvature stop may be imposed over structure 100. Referring to **Figure 11**, once interconnection element 185 curves away from substrate 110 and strikes curvature stop 187 (shown in ghost lines), the curvature stop will prevent further curvature away from the substrate (i.e., towards its secured base). In this manner, materials may be chosen having volume transformation properties much greater than might otherwise be chosen in the absence of curvature stop 187. For example, in order to introduce a plurality of interconnection elements on substrate 110, each of similar form and having a deflection distance, h_1 , of about 8 mils (about 200 μm), the materials (e.g., first element material 170 and second element material 180) may be chosen of a type and amount such that the inherent deflection distance on, for example, thermal transformation is greater than 8 mils (e.g., 10-12 mils (about 250-300 μm)). Curvature stop 187, such as planar

substrate may be placed at a distance of about 8 mils (about 200 μm) from the surface of substrate 110 to limit the deflection distance of the interconnection elements during transformation. Thus, where curvature stop 187 limits, for example, a z-axis deformation (deflection), the interconnection elements may still deform in x- and y-coordinate planes. The final interconnection elements will consistently have a height of about 8 mils (about 200 μm) from the surface of substrate 110.

Heretofore, the discussion has focused on a second element material that overlies a first element material and undergoes a transformation to transform to a smaller volume or to a different stress level and cause the free end of the interconnection element to curve away from the substrate. It is to be appreciated that other configurations may achieve similar results. For example, the second element material may underlie the first element material and have a property such that thermal transformation of the second element material will increase the volume of the material. In such case, similar curvature of the interconnection away from the substrate should be achieved. Likewise, a first element material may be selected such that it undergoes a volume transformation compared with a second element material having a relatively constant volume. Other configurations such as where both materials undergo a volume transformation or have different thermal expansion properties will behave similarly as will shape memory alloys (SMAs).

Figure 12 shows structure 100 after the deposition of third masking material layer 190 conformally over substrate 110. A suitable thickness for third masking material layer 190 is about 0.3-1.5 mils (about 8 to 30 μm). In one embodiment, third masking material layer 190 is selected such

that the conformal deposition will provide adequate masking of the entire portion of the superior surface of substrate 110 as well as coverage over interconnection element 185. In certain instances, the structural stability of

- 5 interconnection element 185 is not sufficient to sustain a masking material layer deposited, for example, by spin-coating. Additional deposition techniques known in the art can be considered for this application. CVD or PECVD (plasma enhanced CVD) may be particularly useful for deposition.
- 10 Other deposition techniques, such as sputter deposition are also not generally suitable for providing sufficient edge or surface coverage. Many introduction techniques will not mask any of the underside of interconnection element 185 nor the surface of substrate 110 nor shorting layer 130 where
- 15 interconnection element 185 inhibits such introduction.

- In one aspect, a positive electrophoretic resist is introduced as third masking material layer 190. Generally, an electrophoretic resist is an electro-deposited photoresist in which charged particles on the order of 1000 to 2000
- 20 angstroms (Å) are introduced in a uniform coating over a conductive surface. A suitable positive electrophoretic resist is Electro Image®, commercially available from PPG Industries of Pittsburgh, Pennsylvania. A second alternative is applying positive photoresist by spray-on method that will
- 25 not damage the composite interconnection element. In either case, third masking material layer 190 is chosen, in one aspect, to have a thickness sufficient to define an additional layer substantially over the interconnection element as well as provide sufficient (in one embodiment,
- 30 total) masking of interconnection element 185 such that additional interconnection materials will be introduced only onto an exposed area of interconnection element 185 and not

over the surface of substrate 110 (e.g., not under interconnection element 185).

Figure 13 shows structure 100 being exposed to open an area over a superior surface of the composite interconnection elements. In the embodiment illustrated, mask 200 is aligned over structure 100 having openings 210 corresponding with the superior surfaces of interconnection elements 185. In this embodiment, developing proceeds like a conventional positive photoresist. After developing, third masking material layer 190 will remain in all areas except the superior surface of the interconnection element 185. **Figure 14** illustrates the structure after development.

Figure 15 shows structure 100 after the introduction of third element material 220 over interconnection element 185. In one embodiment, third element material 220 is a conductive material of, for example, a nickel alloy such as nickel-cobalt (e.g., 70 percent nickel-30 percent cobalt) possibly with an additive such as discussed in detail in co-pending, commonly-assigned U.S. Patent Application No. 09/217,589, filed December 22, 1998, titled "Method of Making a Product with Improved Material Properties by Moderate Heat Treatment of a Metal Incorporating a Dilute Additive" (FFI-P036D1), and the corresponding PCT Application No. WO99/14404, published March 25, 1999, which is incorporated herein by reference that provides increased spring force to interconnection element 185 (i.e., a "spring material"). In general, third element material 220 is introduced to a thickness suitable for increasing the spring constant of interconnection element 185. In one embodiment, third element material 220 is introduced to a thickness of, for example, about 1 mil (about 25 μm) to account for more than 90 percent of the body of interconnection element 185. Collectively, interconnection

element 185 including third element material 220 define an interconnection element having a spring constant and a measurable resiliency greater than interconnection element 185 without third element material 220 (e.g., the spring constant generally depending in part on the dimensions of the interconnection element). In embodiments of interconnection elements useful in making electrical connections with, for example, contact pads or terminals of electronic components fabricated according to current technology, a spring constant of about 0.2 gram-force per mil or greater is suitable. It is to be appreciated that the desired spring constant may vary according to the desired application such as spring constants between about 0.01 and 10 gram-force per mil and preferably about 0.01 and 2 gram-force per mil. These parameters are only illustrative as one skilled in the art can make very thin or very thick structures with a wide range of spring constants.

It is to be appreciated that interconnection element 185 may already possess the desired spring constant, even a spring constant between about 0.01 and 10 gram-force per mil, rendering the inclusion of additional spring material unnecessary. For example, in the embodiment described above where the deformation of the interconnection element was brought about by relaxing the stress of the material (e.g., to reduce the magnitude of stress in first element material 170 and second element material 180), a sufficient amount of spring material, such as, for example, 0.5 to 1 mil (about 12 to 25 μm) of first element material 170 may be introduced, eliminating the need to introduce more spring material at this point.

In the embodiment where third element material 220 is a nickel alloy, such as nickel-cobalt, third element material

220 may be deposited by several deposition techniques, including but not limited to, electroplating, chemical vapor deposition, sputter deposition, and electroless plating. In one example, third element material 220 is deposited through an electroplating process. Third element material 220 is typically applied in the form of a commercially available electroplate solution or bath. Next, a current is applied between interconnection element 185 and an anode of an electroplating cell (not shown). Negative charge build-up on interconnection element 185 causes metal ions in the electroplating solution to be reduced to a metallic state, coating third element material 220 on interconnection element 185.

In certain instances involving electrolytic introduction of materials it may not be desirable to introduce through plating third element material 220 onto second element material 180 of an untreated Ni/Co alloy. Nickel or a Ni/Co alloy is generally not a good receiving surface for a plating process because of its tendency to oxidize. Techniques to improve nickel's or Ni/Co alloy's receiving properties include introducing (e.g., plating) a thin layer of copper (Cu), on the order of 10-100 micro-inches (about 3 μm) to second element material 180 immediately after the introduction of second element material 180 (e.g., prior to a significant amount of oxidation of the Ni/Co). Alternatively, after introducing second element material 180, interconnection element 185 may be placed in a bath of hydrochloric acid (HCl), a "strike bath", to activate the surface of the Ni/Co alloy, then third element material 220 may be introduced.

Figure 16 shows structure 100 after the optional introduction of contact material 230 on the surface of third

element material 220. In one example, contact material 230 reduces the resistivity of third element material 220 and provides contact metallurgy to the interconnection structure that is formed. Suitable contact materials include, but are not limited to, gold (Au), palladium (Pd), rhodium (Rh), and silver (Ag), and their alloys. In one embodiment, contact material 230 is introduced to a thickness of approximately 30 micro-inches (about 0.75 μm) to the surface of third element material 220.

Figure 17 shows structure 100 after removal of third masking material layer 190. In the embodiment where third masking material layer 190 is a photoresist, third masking material layer 190 may be removed using conventional methods, such as plasma etching (e.g., oxygen ashing), laser ablation, or wet chemical etching. After the removal of third masking material layer 190, interconnection element 185 having a free-standing portion is formed on substrate 110. In the embodiment described, interconnection element 185 is a composite interconnection element comprised of activation layer 150, first element material 170, second element material 180, third element material 220, and contact material 230. At this point, structure 100 may be subjected to a heat treatment to improve the properties of interconnection elements 185. Such a heat treatment is described in detail in copending U.S. Patent Application No. 09/217,589, filed December 22, 1998, titled "Method of Making a Product with Improved Material Properties by Moderate Heat Treatment of a Metal Incorporating a Dilute Additive" (FFI-P036D1) and assigned to assignee of this application, including a moderate heat treatment of, for example, 350°C for ten minutes or 300°C for 60 minutes for improving the properties of a Ni-Co alloy. As noted in the referenced

document, different heat treatment schedules may be appropriate for different materials. Also, at this point, exposed portions of shorting layer 130 are removed using methods such as plasma etching (e.g., oxygen ashing), laser ablation, or wet chemical etching. Alternatively, shorting layer 130 may be patterned, at this point, into appropriate traces via a solvent, etching agent, or mechanical means.

Figure 18 shows structure 100 after the introduction of travel stop material 240 over and around the structure.

Travel stop material 240 is introduced to limit the travel of the composite interconnection element when making contact with, for example, a contact pad or terminal of an electronic component. In one embodiment, referring to **Figure 17**, the height, h_2 , of the tip of the composite interconnection element from the surface of substrate 110 is approximately 5-15 mils (about 250-400 μm). The desired amount of deflection for interconnection element 185 may be determined to be approximately 3 mils (about 75 μm). Thus, in one embodiment, travel stop material 240 is deposited to a thickness of approximately 2-12 mils (about 50-300 μm) to provide a height, h_2 , of interconnection element 185 over travel stop material 240 of approximately 3 mils (about 75 μm). In one embodiment, travel stop material 240 is a non-conductive material such as a photoimageable material such as SU-8, which is a negative photoresist, commercially available from MicroChem Corporation of Newton, Massachusetts. Other material for travel stop material 240 includes silicon nitride or silicon oxynitride or Probelec® from Ciba Specialty Chemicals (Basel, Switzerland, www.cibasc.com). A detailed discussion of suitable travel stop configurations and their uses is presented in co-pending, commonly-assigned U.S. Patent Application No. 09/114,586, filed July 13, 1998,

titled "Interconnected Assemblies and Methods" (FFI-P056), and corresponding PCT Application No. US99/00322, filed January 4, 1999, which are incorporated herein by reference.

After the proper amount of travel stop material 240 is introduced over substrate 110, travel stop material 240 is patterned. In the example of a negative photoresist, travel stop material 240 is exposed through a mask such that an area around anchor portion 112 is exposed. Because travel stop material 240 is a negative photoresist, those areas that did not receive an exposure due to the mask will be developed to remove the photoresist (and the exposed portions of the photoresist will remain). **Figure 19** shows structure 100 after patterning travel stop material 240 over a portion of substrate 110 including anchor portion 112 of interconnection elements 185. In this embodiment, travel stop material 240 will allow the deflection of interconnection elements 185 towards substrate 110 with some limitation. For example, when a second electronic component is directed towards substrate 110, the electronic component will be stopped by travel stop material 240.

Figure 20 shows interconnection element 185 having an overcoating of contact material 250, such as electroless gold (Au) coated to a few micro-inches (about 1-10 μm). This can improve the electrical conduction properties (e.g., lower the resistance) of the interconnection element and give additional corrosion resistance. It is to be appreciated that coating 250 is optional in this embodiment.

In the embodiment described, interconnection element 185 is a composite interconnection element. It is to be appreciated that the formation of interconnection element 185 of the core of first element material 170 and second element material 180 may be sufficient for certain applications and

the introduction of additional resilient material (e.g., third element material 220) is unnecessary. Such an interconnection element may be defined possibly with an over-coating of contact material (e.g., Au). At the same time, it is also to be appreciated that additional element materials may be added beyond third element material 220 to, for example, increase the resiliency of the interconnection elements. In this manner, process steps similar to those described with the introduction of third element material 220 may be employed. In those instances, concerns of damaging the interconnection element in the application of additional masking material layer(s) are generally not as significant so more traditional forms of masking material may be utilized.

Figures 22-27 illustrate an additional embodiment of forming an interconnection element according to the invention, starting from the structure illustrated in **Figure 6** showing first element material 170 overlying activation layer 150 patterned according to the pattern established by second masking material layer 160. In one example, first element material 170 is a thermally-stable material such as a Pd/Co alloy. In **Figure 22**, second masking material layer 160 is removed, for example, by an oxygen plasma etch (e.g., oxygen ashing), wet chemical etching, or laser ablation.

Figure 23 shows structure 100 after the introduction of shape memory alloy (SMA) material 380. SMAs are groups of metallic materials that demonstrate the ability to return to some previously defined shape or size when subjected to the appropriate procedure, typically a thermal procedure. Such materials can recover substantial strain or generate significant force upon changing shape. Certain nickel-titanium alloys and copper-based alloys meet this criteria.

The copper-based alloys (such as copper-zinc alloys) are particularly suitable for plating operations.

In one example, SMA material 380 is stretched from a memory state or shape to a martinsite (low-temperature) state, i.e., in this example, the martinsite state is a stretched state. In this martinsite state, SMA material 380 that is a sheet has greater dimensions, for example, in its length and width than in a memory state.

As shown in **Figure 23**, SMA material 380 is laminated over first element material 170 using, for example, an adhesive such as an epoxy or a eutectic method such as with a gold eutectic. Then, as shown in **Figure 24**, laminated SMA material 380 in its martinsite state is patterned, using, for example, photolithography techniques, into beams overlying patterned first element material 170. Next, as shown in **Figure 25**, first masking material layer 140 is removed by a suitable technique to reveal a plurality of interconnection elements of first element material 170 each having a beam of SMA material 380 laminated thereto, a cantilever beam of each structure separated from the surface of substrate 110 by a height, h_3 . In an insert, **Figure 25** shows an exploded side view of a single beam of SMA material 380, having a length, a , and a thickness, t , in its martinsite or stressed state.

Figure 26 shows a structure after a heat treatment to, in one sense, transition SMA material 380 from its martinsite state to a high-temperature austensite phase or memory state. **Figure 26** includes an insert of SMA material 380 in its memory state, having a length, a' , and thickness, t' . In a memory state, SMA material 380 has different dimensions than in its martinsite state. The insert for **Figure 26** shows SMA material 380 in its memory state having length ($a' < a$) and a width less than a width in a martinsite or stressed state.

To bring about the transition of SMA material 380 to its austensite phase (memory state), the structure is subjected to a heat treatment at a temperature above the transition temperature of SMA material 380 for the austensite phase.

- 5 Above the transition temperature, SMA material 380 will deform back towards its memory state thereby shrinking the film. Through bimorphic or bi-metallic action, the end of the composite beam of SMA material 380 and first element material 170 will, in this instance, bend toward its base.
- 10 In this manner, a desired curvature of an interconnection element may be achieved, such as a height, h_4 of approximately 8 mils (about 200 μm) from the surface of substrate 110. In some instances, it may be desirable to limit the amount of curvature of the interconnection element by, for example, a
- 15 curvature stop such as described above with reference to **Figure 11** and the accompanying text.

- In the above embodiment, the transition of SMA material 380 to its memory state is a thermal transformation. It is to be appreciated that it may be possible to incorporate
- 20 alternative external stimuli (e.g., pressure changes or radiation) to bring about the transformation. In the case of a thermal transformation at elevated temperature, the temperature treatment that brought about such thermal transformation may be extended to anneal first element
- 25 material 170 to modify the stress of the material and increase the stability of the deformed material.

- Following the transition of SMA material 380 to its memory state and the deformation of first element material 170, in one example, SMA material 380 may be removed, leaving
- 30 an interconnection element of first element material 170 (and activation layer 150). In one instance, SMA material 380 may be removed directly by way of a plasma or chemical etch or

laser ablation. Alternatively, where an adhesive is utilized between SMA material 380 and first element material 170, the adhesive may be dissolved to separate SMA material 380 from the first element material 170. **Figure 27** shows the

5 resultant structure wherein SMA material 380 is removed leaving first interconnection element of first element material 170.

In still another embodiment, SMA material can be left on. The stiffness of the SMA material will contribute to the overall spring constant, giving a spring constant which is high enough, without the addition of more material.

10 Separately, a thickness of SMA material can be selected which will deflect a relatively thick beam (material 170). Such a thick beam can be conditioned to retain this new shape and the SMA material can be removed, such that the resulting structure will have a sufficiently high spring constant.

15 **Figure 28** shows one final structure formed according to this embodiment. The structure is similar to **Figure 20**, and shows a cross-sectional side view of three interconnection elements formed on the surface of substrate 110. Each interconnection element includes first element material 170 and third element material 220 of, for example, a nickel alloy such Ni/Co (e.g., 70 percent Ni-30 percent Co), possibly with an additive, that provides increased spring force to the interconnection element (i.e., a "spring material"). In general, third element material 220 is introduced to a thickness suitable for increasing the spring constant of the interconnection element to a suitable level. Thicknesses on the order of about 1 mil (about 25 μm) which accounts for more than 90 percent of the body of the interconnection element are suitable according to current technology. **Figure 28** also shows optional contact material

230 of, for example, gold (Au), palladium (Pd), rhodium (Rh), and silver (Ag), and their alloys introduced to a thickness of approximately 30 micro-inches (about 0.75 μm). Also shown in this illustration is optional contact material 250, such as electroless gold (Au) coated to a few micro-inches (about 1-10 μm). Finally, **Figure 28** shows optional travel stop material 240 that may be used to, in one instance, limit the deflection of the interconnection element. The introduction of each of these materials is similar to that described above with reference to **Figures 12-20** and the accompanying text.

The preceding description illustrates, among other aspects, interconnection elements and the fabrication of interconnection elements with a desired shape and spring constant. The fabrication techniques take advantage of the behavior of certain metallic materials with transformable properties to deform in response to an external stimulus (e.g., heat) to form the improved interconnection elements.

The interconnection elements described herein are suitable for many uses including, but not limited to employment with an electronic component to make temporary or permanent contact, or temporary and then permanent contact to the electronic component. Such contact may be utilized in manufacturing flows in, for example, the formation of integrated circuit chips or dies. For example, the interconnection elements of the invention may form temporary contacts to bond pads of a singulated or unsingulated chip or die as part of "burn-in" and/or functional testing of the chip or die to link the chip or die to a testing device. An example of a permanent connection is the use of the interconnection elements of the invention, possibly including the interconnection elements utilized to make temporary connection while testing a chip or die, to electrically link

the chip or die to external system components (e.g., a PCB) through, for example, a package.

Manufacturing flows are described in detail, for example, in commonly-assigned U.S. Patent No. 5,829,128, which is incorporated herein by reference. The interconnection elements of the invention may be utilized in testing equipment such as extending as probes from a substrate, such as a probe card or a space transformer of a probe card for testing electronic components as described in U.S. Patent No. 5,829,128 and commonly-assigned U.S. Patent No. 5,974,662, also incorporated herein by reference. The interconnection elements of the invention may also be utilized in interposer applications to connect two electronic components as described in U.S. Patent No. 5,829,128.

U.S. Patent No. 5,829,128 further describes interconnection elements connected directly to a semiconductor die (e.g., to bond pads) eliminating the need for a corresponding package. The interconnection elements described herein are likewise suitable for such an application. Commonly-assigned U.S. Patent No. 5,772,451, incorporated herein by reference, describes socket applications suitable for the interconnection applications described herein permitting an electronic component (e.g., a semiconductor package) to be releasably mounted to a circuit board. One example is interconnection elements coupled to a first substrate are coupled, for example, through pressure connections, to corresponding ones of contact nodes on a second substrate (e.g., a support substrate). The second contact nodes include external connection points to couple the collective assembly (e.g., first and second substrates) to corresponding third contact nodes on a third substrate such as a circuit board.

In the preceding detailed description, the invention is described with reference to specific embodiments thereof. It will, however, be evident that various modifications and changes may be made thereto without departing from the
5 broader spirit and scope of the invention as set forth in the claims. The specification and drawings are, accordingly, to be regarded in an illustrative rather than a restrictive sense.

CLAIMS

What is claimed is:

- 1 1. An interconnection element comprising:
2 a first element material adapted to be coupled to a
3 substrate; and
4 a second element material coupled to the first element
5 material,
6 wherein one of the first element material and the second
7 element material comprises a material having a transformable
8 property such that upon transformation, a shape of the
9 interconnection element is modified.
- 1 2. The interconnection element of claim 1, wherein the
2 interconnection element is of a size suitable for directly
3 contacting a semiconductor device.
- 1 3. The interconnection element of claim 1, wherein a
2 transformation of the one of the first element material and
3 the second element material is irreversible.
- 1 4. The interconnection element of claim 1, wherein the
2 transformable property is such that a first volume of the
3 material is adapted to be transformed to a different second
4 volume.
- 1 5. The interconnection element of claim 4, wherein the
2 first element material and the second element material are
3 arranged in a configuration such that the second element
4 material overlies the first element material and the first
5 volume of the second element material is greater than the
6 second volume.
- 1 6. The interconnection element of claim 4, wherein the
2 first element material is thermally stable and a
3 transformation of the second element material from the first

4 volume to the second volume is a result of exposing the
5 second element material to heat.

1 7. The interconnection element of claim 6, wherein the
2 transformation comprises at least about 90 percent of the
3 transformable volume change of the second element material.

1 8. The interconnection element of claim 1, wherein each of
2 the first element material and the second element material
3 have a transformable property.

1 9. The interconnection element of claim 1, wherein at least
2 one of the firsts element material and the second element
3 material are introduced by plating.

1 10. The interconnection element of claim 1, wherein at least
2 one of the first element material and the second element
3 material are introduced by sputtering.

1 11. The interconnection element of claim 1, wherein at least
2 one of the first element material and the second element
3 material are introduced by electroless plating.

1 12. The interconnection element of claim 3, wherein the
2 first element material comprises palladium or its alloy.

1 13. The interconnection element of claim 4, wherein the
2 first element material is an alloy comprising
3 palladium/cobalt and the activation layer comprises one of
4 copper and nickel.

1 14. The interconnection element of claim 13, wherein the
2 second element material further comprises nickel.

1 15. The interconnection element of claim 13, wherein the
2 second element material comprises a nickel alloy.

- 1 16. The interconnection element of claim 1, wherein the one
2 of the first element material and the second element material
3 comprises a shape memory alloy.
- 1 17. The interconnection element of claim 16, wherein the
2 second element material comprises the shape memory alloy and
3 overlies the first element material.
- 1 18. The interconnection element of claim 1, wherein the
2 transformable property is a stress and the transformation
3 reduces the magnitude of the stress of the material.
- 1 19. The interconnection element of claim 18, wherein the
2 first element material comprises the material having the
3 transformable property and the second element material has a
4 tensile stress, wherein upon transformation, the deformation
5 comprises a response to the tensile stress of the second
6 element material.
- 1 20. The interconnection element of claim 19, wherein the
2 second element material is thermally stable and a
3 transformation of the first element material is a result of
4 exposing the first element material to heat.
- 1 21. The interconnection element of claim 18, wherein the
2 first element material comprises the material having the
3 transformable property and the second element material has a
4 compressive stress, wherein upon transformation, the
5 deformation comprises a response to the compressive stress of
6 the second element material.
- 1 22. An electronic component comprising:
2 a substrate with a plurality of contact nodes; and

3 a plurality of free-standing resilient interconnection
4 elements coupled to the substrate in such a manner that a
5 base of an interconnection element electrically contacts a
6 corresponding one of the contact nodes and an interconnection
7 element comprises:

8 a first element material adapted to be coupled to a
9 substrate, and

10 a second element material coupled to the first
11 element material,

12 wherein one of the first element material and the second
13 element material comprises a material having a transformable
14 property such that upon transformation, a shape of the
15 interconnection element is modified.

1 23. The electronic component of claim 22, further
2 comprising:

3 a plurality of conductive signal lines associated with
4 the substrate; and

5 in the plurality of free-standing resilient
6 interconnection elements coupled to the substrate, the base
7 of the interconnection element electrically contacts a
8 corresponding one of the signal lines and an interconnection
9 element.

1 24. The electronic component of claim 22, wherein a
2 transformation of the one of the first element material and
3 the second element material is irreversible.

1 25. The electronic component of claim 24, wherein the
2 transformable property is such that a first volume of the
3 second element material is adapted to be transformed to a
4 different second volume.

1 26. The electronic component of claim 25, wherein a free
2 portion of the interconnection element is initially fixed to
3 the substrate and when the free portion is released from the
4 substrate, the free portion is adapted to be biased away from
5 the substrate in response to a transformation of the second
6 element material from the first volume to the second volume.

1 27. The electronic component of claim 26, wherein the first
2 element material and the second element material of the
3 interconnection element are arranged in a configuration such
4 that the second element material overlies the first element
5 material and the first volume is greater than the second
6 volume.

1 28. The electronic component of claim 27, wherein the first
2 element material is thermally stable and a transformation of
3 the second element material from the first volume to the
4 second volume is a result of exposing the second element
5 material to heat.

1 29. The electronic component of claim 28, wherein the
2 transformation comprises at least 90 percent of a
3 transformable volume change of the second element material.

1 30. The electronic component of claim 22, wherein each of
2 the first element material and the second element material
3 have a transformable property.

1 31. The electronic component of claim 22, wherein at least
2 one of the first element material and the second element
3 material are introduced by plating.

32. The electronic component of claim 22, wherein at least one of the first element material and the second element material are introduced by sputtering.

33. The electronic component of claim 22, wherein the first element material comprises palladium.

34. The electronic component of claim 22, wherein the second element material overlies the first element material and each interconnection element further comprises a spring material coupled to the second element material, the spring material comprising at least about 90 percent of the interconnection element.

35. The electronic component of claim 34, wherein the spring material comprises a nickel alloy.

36. The electronic component of claim 34, wherein the interconnection element further comprises a contact material adjacent a surface of the spring material.

37. The electronic component of claim 22, wherein the one of the first element material and the second element material comprises a shape memory alloy.

38. The electronic component of claim 22, wherein the transformable property is a stress and the transformation reduces the magnitude of the stress of the material.

39. The electronic component of claim 38, wherein the first element material comprises the material having the transformable property and the second element material has a tensile stress, wherein upon transformation, the deformation comprises a response to the tensile stress of the second element material.

1 40. The electronic component of claim 39, wherein the second
2 element material is thermally stable and a transformation of
3 the first element material is a result of exposing the first
4 element material to heat.

1 41. The electronic component of claim 38, wherein the first
2 element material comprises the material having the
3 transformable property and the second element material has a
4 compressive stress, wherein upon transformation, the
5 deformation comprises a response to the compressive stress of
6 the second element material.

1 42. The electronic component of claim 22, wherein the
2 plurality of free standing interconnection elements are
3 coupled to more than one surface of the substrate.

1 43. The electronic component of claim 22, wherein the
2 plurality of nodes comprise a first contact points, the
3 electronic assembly further comprising:

4 at least one re-distribution line coupled to at least
5 one of the plurality of nodes,

6 wherein the corresponding at least one interconnection
7 element is coupled to a second contact point different from
8 the first contact point, the second contact point and the
9 first contact point coupled through the at least one re-
10 distribution line.

1 44. The electronic component of claim 22, wherein the
2 substrate comprises one of a semiconductor, a ceramic, an
3 organic, and a metal material.

1 45. The electronic component of claim 22, wherein the
2 substrate comprises an interposer.

1 46. The electronic component of claim 22, wherein the
2 substrate comprises a component of a probe card.

47. The electronic component of claim 22, wherein the substrate comprises a socket for releasably connecting the electronic assembly to an electronic component.

48. An assembly comprising:

a first substrate having a plurality of first contact nodes formed on the first substrate and a plurality of free-standing resilient interconnection elements coupled to the substrate in such a manner that a base of an interconnection element electrically contacts a corresponding one of the first contact nodes; and

a second substrate having a plurality of second contact nodes,

wherein the interconnection element comprises:

a first element material adapted to be coupled to a substrate, and

a second element material coupled to the first element material, and one of the first element material and the second element material comprises a material having a transformable property such that upon transformation, a shape of the interconnection element is modified,

wherein the interconnection element has a portion thereof which is capable of moving to a first position in which the interconnection element is in contact with a second contact node.

49. The assembly of claim 48, wherein the assembly is part of a probe card assembly.

50. The assembly of claim 48, wherein the assembly is part of a wafer-level test assembly.

51. The assembly of claim 48, wherein the second substrate is a circuit board.

52. The assembly of claim 48, wherein the assembly is part of a socket and the second contact nodes comprise external connection points.

1 53. The assembly of claim 52, further comprising a third
2 substrate of a circuit board having a plurality of third
3 contact nodes,

4 wherein the external connection are aligned with the
5 third contact nodes to couple the package to the third
6 substrate.

1 54. The assembly of claim 48, further comprising a stop
2 structure disposed on the first substrate and defining the
3 first position.

1 55. The assembly of claim 48, wherein a transformation of
2 the one of the first element material and the second element
3 material is irreversible.

1 56. The assembly of claim 55, wherein the transformable
2 property is such that a first volume of the material is
3 adapted to be transformed to a different second volume.

1 57. The assembly of claim 56, wherein a free portion of the
2 interconnection element is initially fixed to the first
3 substrate and when the free portion is released from the
4 first substrate, the free portion is adapted to be biased
5 away from the first substrate in response to a transformation
6 of the second element material from the first volume to the
7 second volume.

1 58. The assembly of claim 57, wherein the first element
2 material and the second element material of the
3 interconnection element are arranged in a configuration such
4 that the second element material overlies the first element
5 material and the first volume is greater than the second
6 volume.

1 59. The assembly of claim 58, wherein the first element
2 material is thermally stable and a transformation of the

3 second element material from the first volume to the second
4 volume is a result of exposing the second element material to
5 heat.

1 60. The assembly of claim 59, wherein the transformation
2 comprises at least 90 percent of a transformable volume
3 change of the second element material.

1 61. The assembly of claim 48, wherein each of the first
2 element material and the second element material have a
3 transformable property.

1 62. The assembly element of claim 48, wherein at least one
2 of the first element material and the second element material
3 are introduced by plating.

1 63. The assembly of claim 48, wherein at least one of the
2 first element material and the second element material are
3 introduced by sputtering.

1 64. The assembly of claim 48, wherein the first element
2 material comprises palladium.

1 65. The assembly of claim 48, wherein the second element
2 material overlies the first element material and
3 interconnection element further comprises a spring material
4 coupled to the second element material, the spring material
5 comprising at least about 90 percent of the interconnection
6 element.

1 66. The assembly of claim 65, wherein the spring material
2 comprises a nickel alloy.

1 67. The assembly of claim 65, wherein the interconnection
2 element further comprises a contact material adjacent a
3 surface of the spring material.

1 68. The assembly of claim 48, wherein the one of the first
2 element material and the second element material comprises a
3 shape memory alloy.

1 69. The assembly of claim 48, wherein the transformable
2 property is a compressive stress and the transformation
3 reduces the compressive stress of the material.

1 70. The assembly of claim 69, wherein the first element
2 material comprises the material having the transformable
3 property and the second element material has a tensile
4 stress, wherein upon transformation, the deformation
5 comprises a response to the tensile stress of the second
6 element material.

1 71. The assembly of claim 70, wherein the second element
2 material is thermally stable and a transformation of the
3 first element material is a result of exposing the first
4 element material to heat.

1 72. The assembly of claim 69, wherein the first element
2 material comprises the material having the transformable
3 property and the second element material has a compressive
4 stress, wherein upon transformation, the deformation
5 comprises a response to the compressive stress of the second
6 element material.

1 73. The assembly of claim 48, wherein the plurality of free
2 standing interconnection elements are coupled to more than
3 one surface of the substrate.

1 74. The assembly of claim 48, wherein each of the plurality
2 of first contact nodes on the first substrate terminate at
3 first contact points, the electronic assembly further
4 comprising:

at least one re-distribution line coupled to at least one of the plurality of first contact nodes,

wherein the corresponding at least one interconnection element is coupled to a second contact point different from the first contact point, the second contact point and the first contact point coupled through the at least one re-distribution line.

75. The assembly of claim 48, wherein the first substrate comprises one of a semiconductor, a ceramic, an organic, and a metal material.

76. A system for contacting an electronic device including an assembly comprising:

a first substrate having a plurality of first contact nodes formed on the first substrate and a plurality of free-standing resilient interconnection elements coupled to the substrate in such a manner that a base of an interconnection element electrically contacts a corresponding one of the first contact nodes; and

a second substrate having a plurality of second contact nodes,

wherein the interconnection element comprises:

a first element material adapted to be coupled to a substrate, and

a second element material coupled to the first element material, and one of the first element material and the second element material comprises a material having a transformable property such that upon transformation, a shape of the interconnection element is modified,

wherein the interconnection element has a portion thereof which is capable of moving to a first position in which the interconnection element is in contact with a second contact node.

- 1 77. The system of claim 76, wherein the system comprises an
2 integrated circuit test system and the assembly is part of a
3 probe card assembly.
- 1 78. The system of claim 76, wherein the system is a
2 substrate mounting system.
- 1 79. The system of claim 78, wherein the second substrate is
2 a circuit board.
- 1 80. The system of claim 78, wherein the assembly is part of
2 a socket and the second contact nodes comprise external
3 connection points.
- 1 81. The system of claim 80, wherein the assembly further
2 comprises a third substrate of a circuit board having a
3 plurality of third contact nodes,
4 wherein the external connection are aligned with the
5 third contact nodes to couple the package to the third
6 substrate.
- 1 82. The system of claim 76, further comprising a stop
2 structure disposed on the first substrate and defining the
3 first position.

ABSTRACT

An interconnection element that includes a first element material adapted to be coupled to a substrate and a second element material comprising a material having a transformable property such that upon transformation, a shape of the interconnection is modified. An example is a material that has a transformable property such that a volume of the first and/or second element material may undergo a thermal transformation from one volume to a different volume (such as a smaller volume).

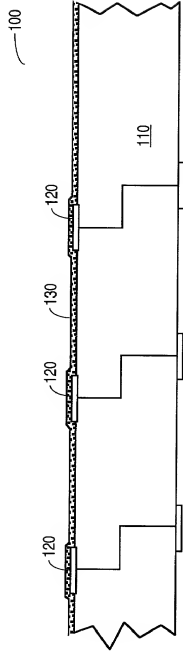


FIG. 1

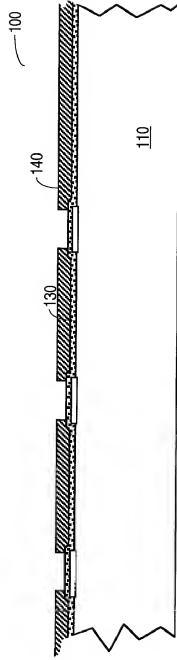


FIG. 2

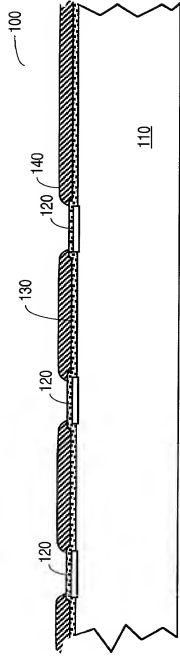


FIG. 3

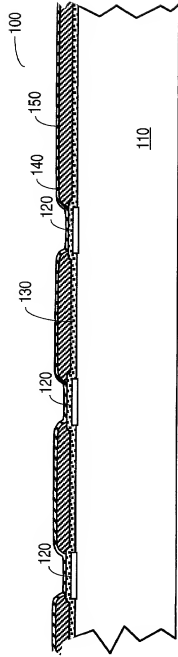


FIG. 4

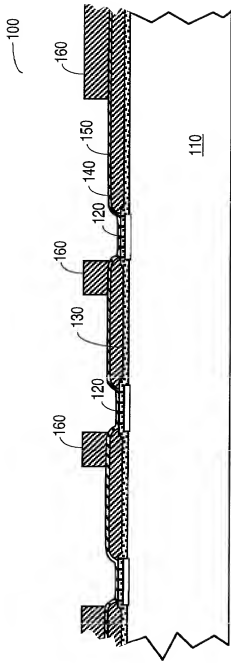


FIG. 5

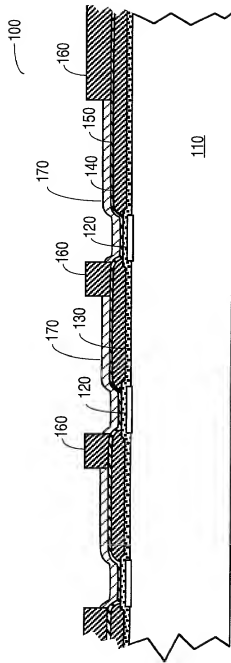


FIG. 6

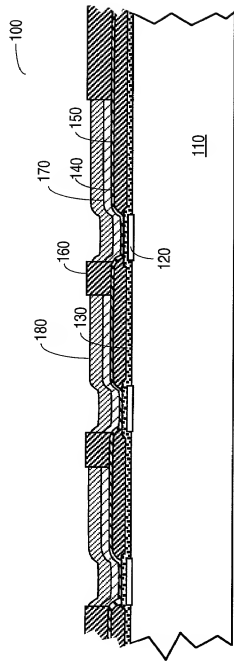


FIG. 7

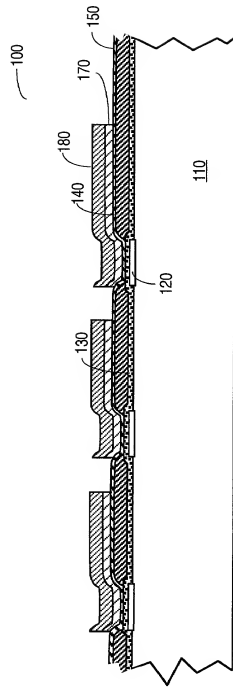


FIG. 8

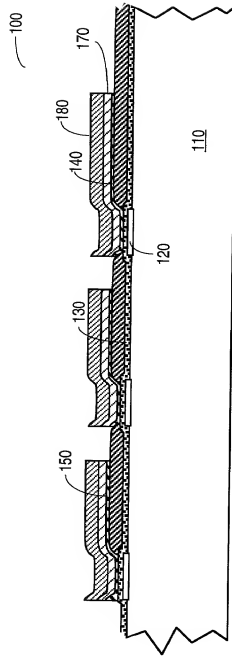


FIG. 9

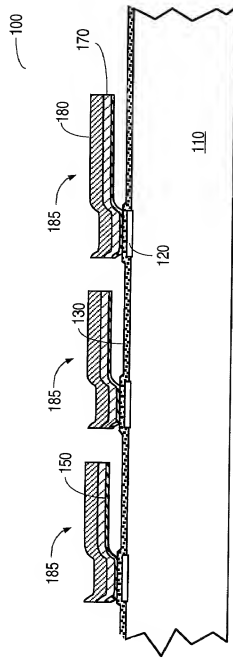


FIG. 10

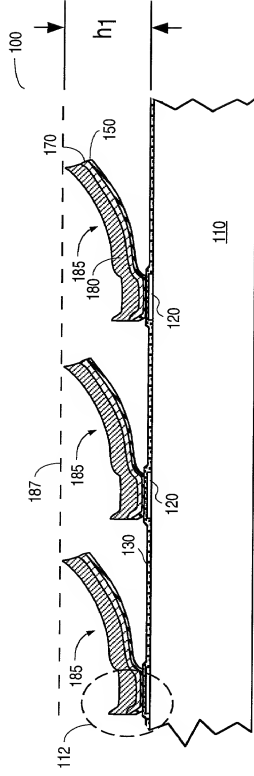


FIG. 11

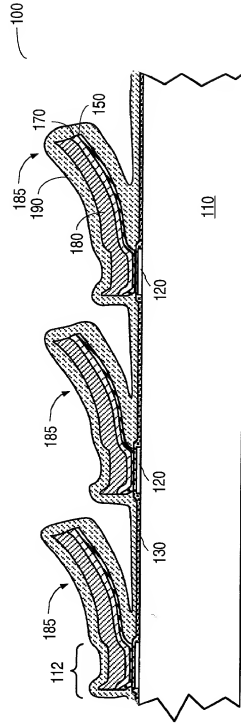


FIG. 12

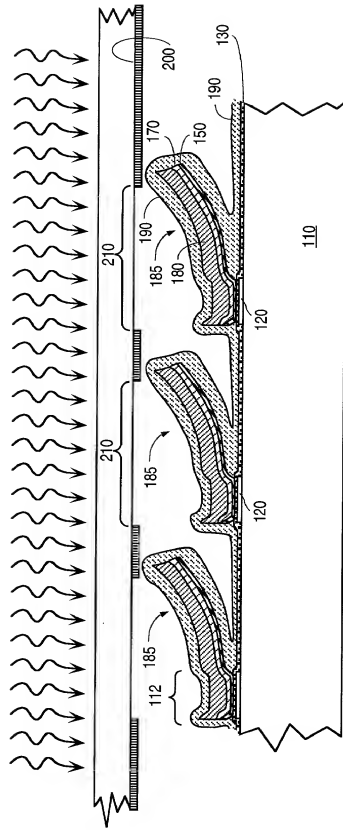


FIG. 13

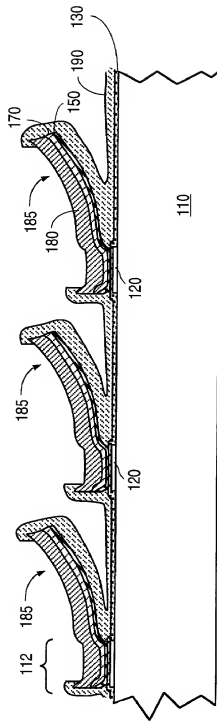


FIG. 14

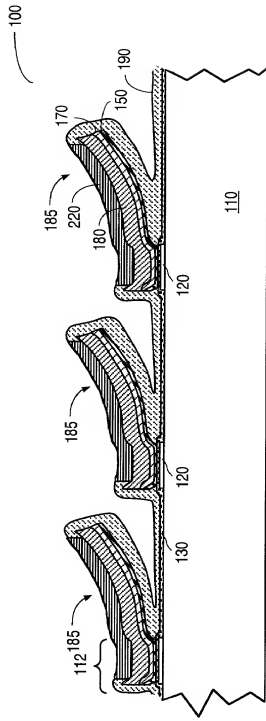


FIG. 15

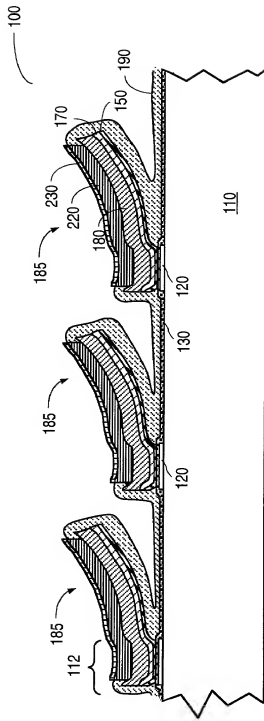


FIG. 16

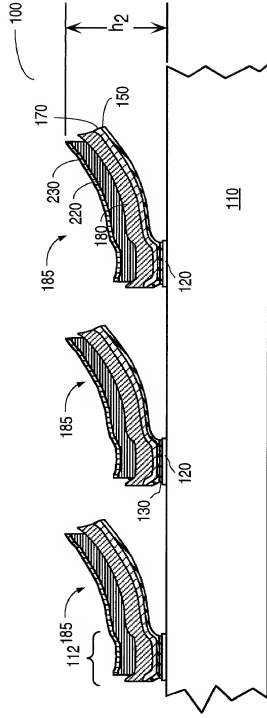


FIG. 17

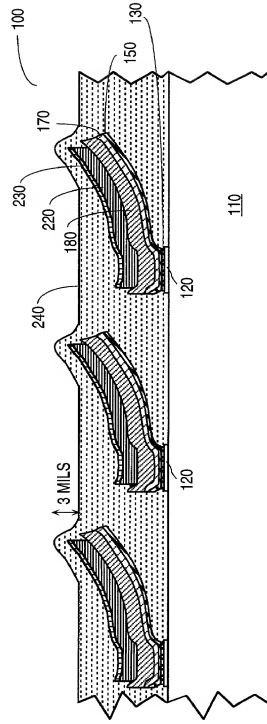


FIG. 18

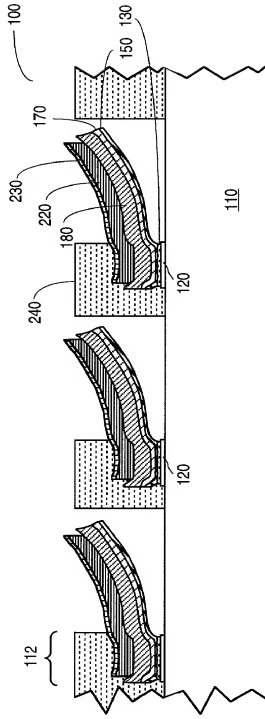


FIG. 19

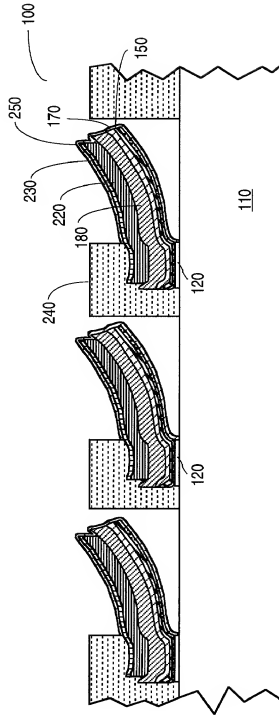


FIG. 20

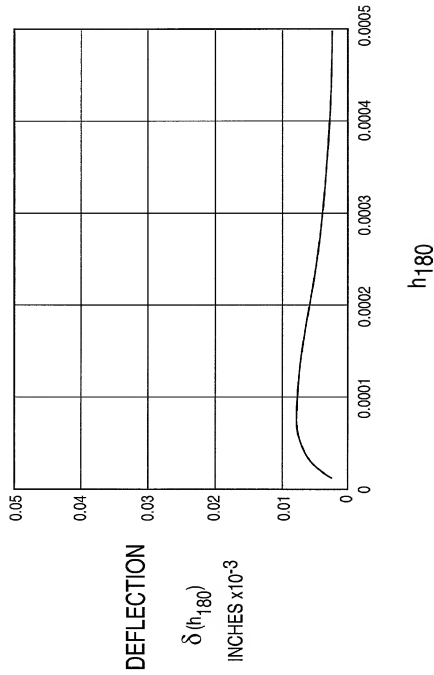


FIG. 21

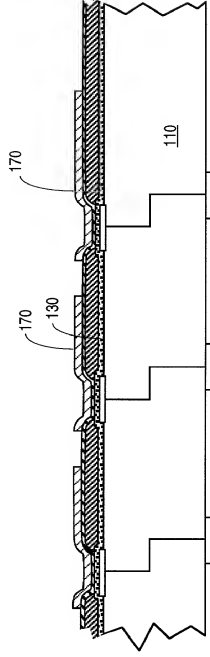


FIG. 22

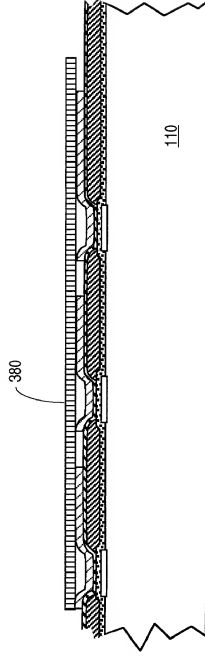


FIG. 23

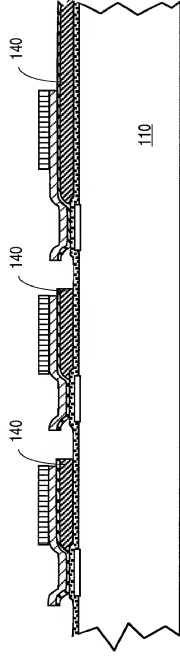


FIG. 24

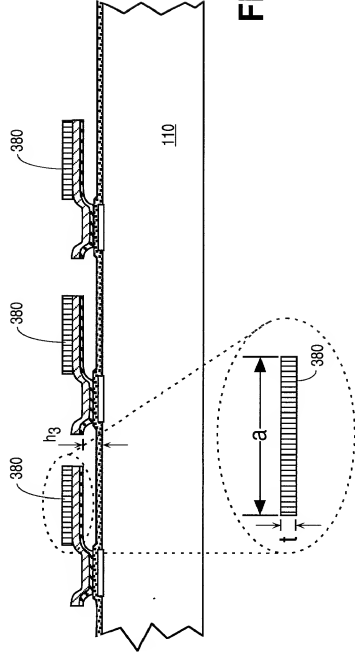


FIG. 25

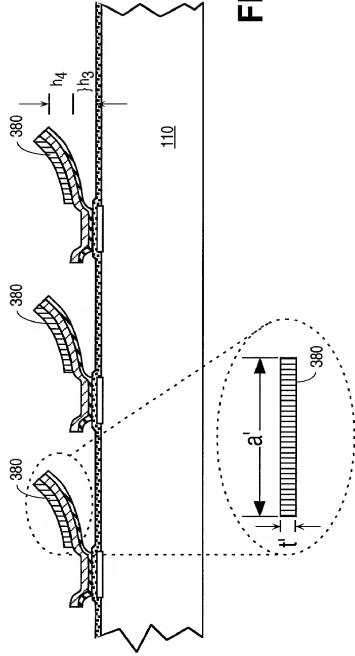


FIG. 26

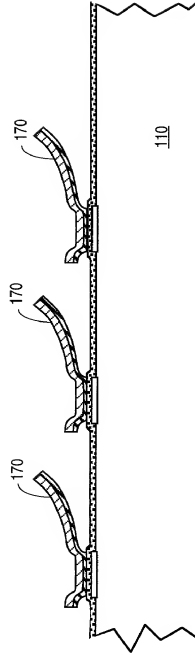


FIG. 27

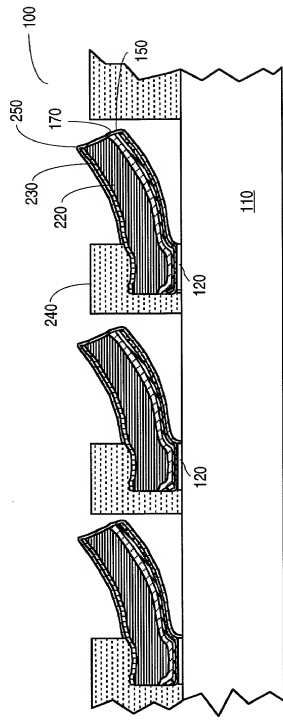


FIG. 28

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

SHAPED SPRING

the specification of which:

☒ is attached hereto
☐ was filed on _____, as
 United States Application No. _____,
 or PCT International Application No. _____
 and was amended on _____
 (if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above. I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (for a utility patent application) or six months (for a design patent application) prior to this application.

I acknowledge the duty to disclose all information I know to be material to patentability as defined in Title 37, Code of Federal Regulation, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d), of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Application Number	Country (or indicate if PCT)	Date of Filing (day, month, year)	Priority Claimed
			<input type="checkbox"/> Yes <input type="checkbox"/> No
			<input type="checkbox"/> Yes <input type="checkbox"/> No
			<input type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below:

Country:	
Application No.	
Filing Date:	


I hereby claim the benefit under Title 35, United States Code §120, of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application(s) in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application(s) and the national or PCT international filing date of this application.

Serial No.	Filing Date	Status (Patented, Pending, Abandoned)

I hereby appoint David J. Larwood, Reg. No. 33,191, my patent attorney of FORMFACTOR, INC. located at 5666 La Ribera Street, Livermore, California 94550, telephone (925) 294-4300, with full power of substitution and revocation, to prosecute this application and to transact all business to the Patent and Trademark Office connected herewith; and

I also hereby appoint William E. Alford, Reg. No. 37,764; Farzad E. Amini, Reg. No. 42,261; Amy M. Armstrong, Reg. No. 42,265; Aloysius T. C. AuYeung, Reg. No. 35,432; William Thomas Babbitt, Reg. No. 39,591; Carol F. Barry, Reg. No. 41,600; Jordan Michael Becker, Reg. No. 39,602; Bradley J. Berezna, Reg. No. 33,474; Michael A. Bernadicon, Reg. No. 35,934; Roger W. Blakely, Jr., Reg. No. 25,831; Gregory D. Caldwell, Reg. No. 39,926; Ronald C. Card, Reg. No. 44,587; Thomas M. Coester, Reg. No. 39,637; Donna Jo Coningsby, Reg. No. 41,684; Michael Anthony DeSanctis, Reg. No. 39,957; Daniel M. De Vos, Reg. No. 37,813; Robert Andrew Diehl, Reg. No. 40,992; Matthew C. Fagan, Reg. No. 37,542; Tarek N. Fahmi, Reg. No. 41,402; Paramita Ghosh, Reg. No. 42,806; James Y. Go, Reg. No. 40,621; James A. Henry, Reg. No. 41,064; Willmore F. Holbrow III, Reg. No. 41,845; Sheryl Sue Holloway, Reg. No. 37,850; George W. Hoover II, Reg. No. 32,992; Eric S. Hyman, Reg. No. 30,139; William W. Kidd, Reg. No. 31,772; Sang Hui Kim, Reg. No. 40,450; Eric T. King, Reg. No. 44,188; Erica W. Kuo, Reg. No. 42,775; Michael J. Mallie, Reg. No. 36,591; Paul A. Mendonsa, Reg. No. 42,879; Darren J. Milliken, Reg. No. 42,004; Chun M. Ng, Reg. No. 36,878; Thien T. Nguyen, Reg. No. 43,835; Thinh V. Nguyen, Reg. No. 42,034; Dennis A. Nicholls, Reg. No. 42,036; Lisa A. Norris, Reg. No. 44,976; Daniel E. Ovanezian, Reg. No. 41,236; William F. Ryann, Reg. No. 44,313; James H. Salter, Reg. No. 35,668; William W. Schaal, Reg. No. 39,018; James C. Scheller, Reg. No. 31,195; Jeffrey S. Smith, Reg. No. 39,377; Maria McCormack Sobrino, Reg. No. 31,639; Stanley W. Sokoloff, Reg. No. 25,128; Judith A. Szepesi, Reg. No. 39,393; Vincent P. Tassinari, Reg. No. 42,179; Edwin H. Taylor, Reg. No. 25,129; Joseph A. Twarowski, Reg. No. 42,191; Lester J. Vincent, Reg. No. 31,460; Glenn E. Von Tersch, Reg. No. 41,364; John Patrick Ward, Reg. No. 40,216; Charles T. J. Weigell, Reg. No. 43,398; James M. Wu, Reg. No. 45,241; Steven D. Yates, Reg. No. 42,242; and Norman Zafman, Reg. No. 26,250; my attorneys; and Andrew C. Chen, Reg. No. 43,544; Justin M. Dillon, Reg. No. 42,486; and John F. Travis, Reg. No. 43,203; my patent agents, of BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP, with offices located at 12400 Wilshire Boulevard, 7th Floor, Los Angeles, California 90025, telephone (310) 207-3800, and James R. Thein, Reg. No. 31,710, my patent attorney; with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and, further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

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